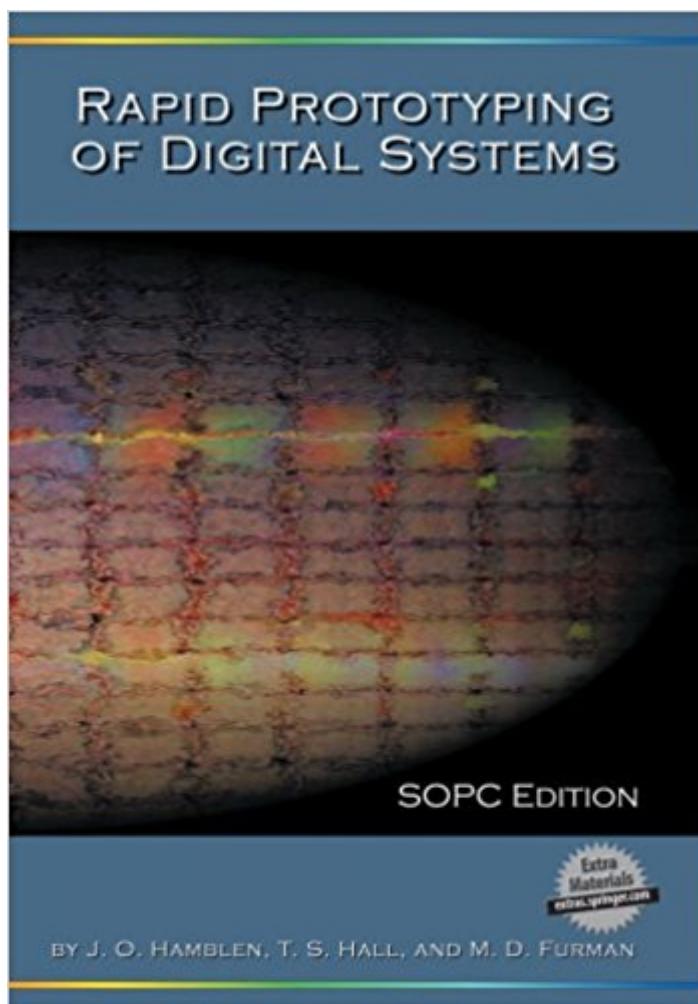


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Rapid Prototyping Of Digital Systems: SOPC Edition



Synopsis

Here is a laboratory workbook filled with interesting and challenging projects for digital logic design and embedded systems classes. The workbook introduces you to fully integrated modern CAD tools, logic simulation, logic synthesis using hardware description languages, design hierarchy, current generation field programmable gate array technology, and SoPC design. Projects cover such areas as serial communications, state machines with video output, video games and graphics, robotics, pipelined RISC processor cores, and designing computer systems using a commercial processor core.

Book Information

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Customer Reviews

RAPID PROTOTYPING OF DIGITAL SYSTEMS provides an exciting and challenging environment for rapidly adapting System-on-a-Programmable Chip (SOPC) technology to existing designs or integrating the new design methods into a laboratory component for digital logic, computer and embedded-design curriculums. New to this edition is an introduction to embedded operating systems for SOPC designs. Featuring four accelerated tutorials on the Quartus II and Nios II design environments, this edition progresses from introductory programmable logic to full-scale SOPC design seamlessly integrating hardware implementation, software development, operating system support, state-of-the-art I/O, and IP cores. This edition features Altera's new 7.1 Quartus II CAD and Nios II SOPC tools and includes projects for Altera's DE1, DE2, UP3, UP2, and UP1 FPGA

development boards.

After 30 years in the ASIC design business and two layoffs, I decided to sharpen my skills by designing with FPGAs. I got an Altera DE1 board and started coding. Then I found this book. I realized that I could do projects ranging from simple digital designs to complex and complete microprocessor based systems. The book lays out coding examples and design flow instructions that enable readers to go beyond a cookbook redo of the sample projects. I am planning to use this book to train engineers that want to learn how to design with FPGAs.

This book was one of the best books I have ever read in VHDL and Verilog areas. Thank you, very satisfied.

Excellent review of material for both beginners and more advanced digital designers. Plenty of examples, tutorials, and code snippets in both VHDL and Verilog. Easy to apply to boards other than Altera DE2.

OK book. It is a bit outdated and the code in the book is considered bad practice. It gets the fundamentals down.

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